

FIG 1

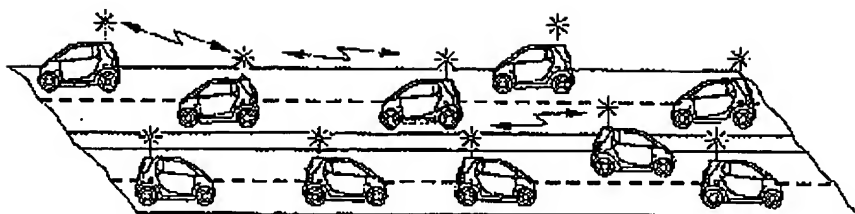


FIG 2

Example 1:

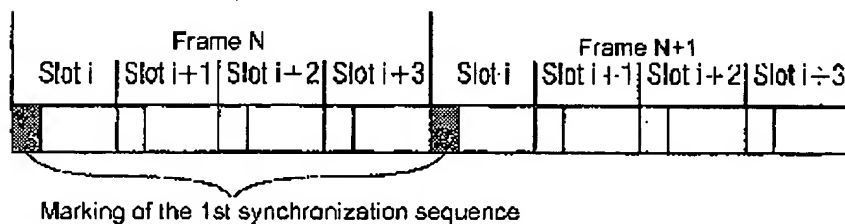
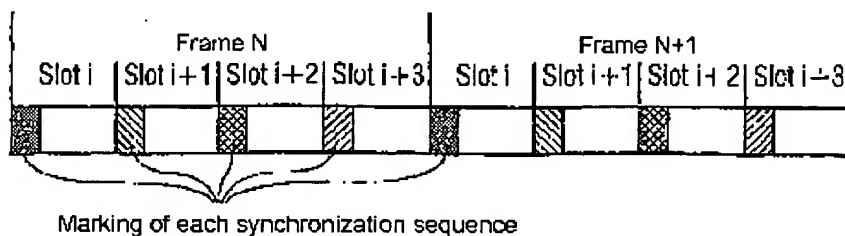


FIG 3

Example 2:



WO 2004/038959

PCT/EP2003/011962

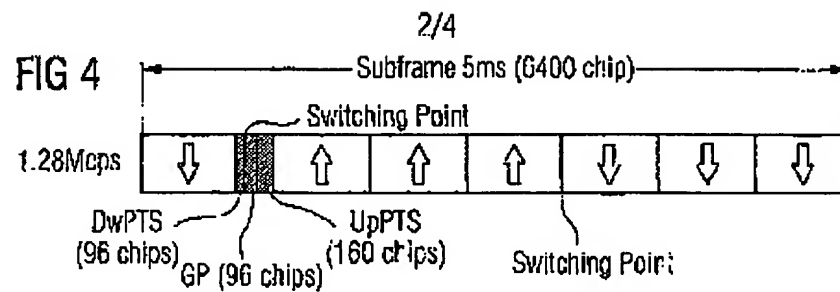


FIG 5A

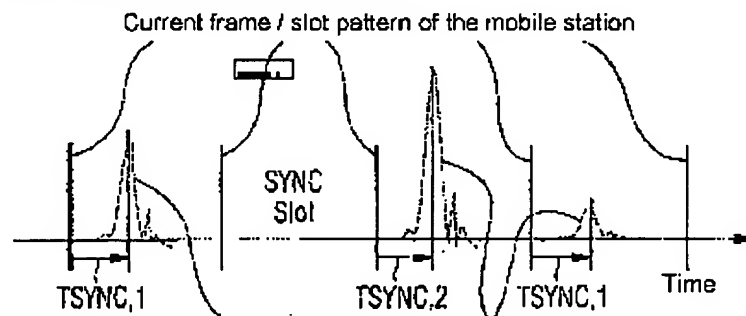
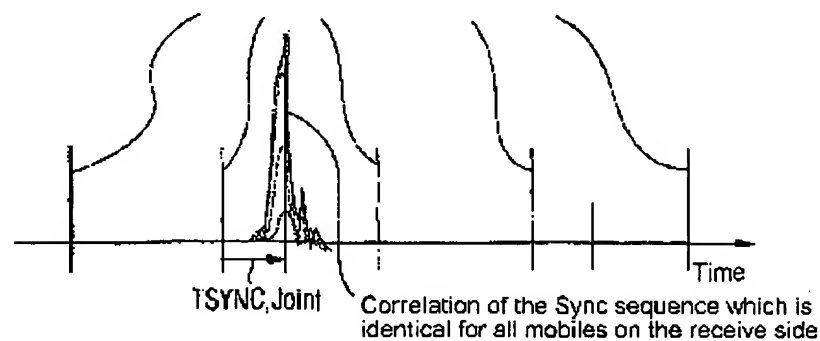
Sequential Decentral Synchronization

FIG 5B

Joint Decentral Synchronization

WO 2004/030959

INT. EP 2003/011962

3/4

FIG 6

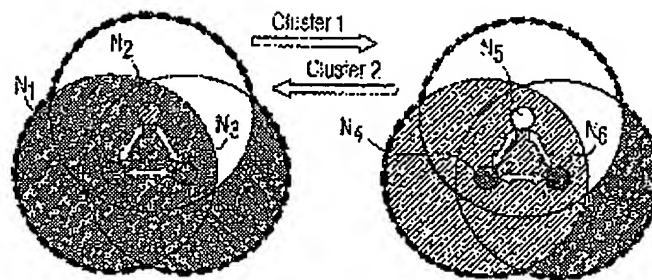
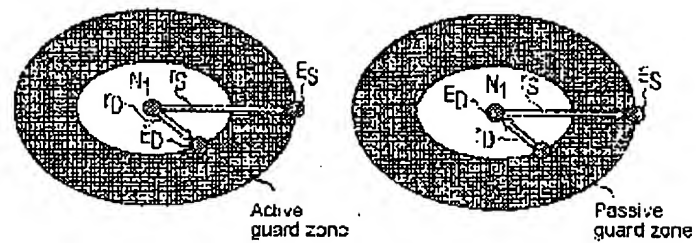


FIG 7



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WO 2004/038959

PCT/EP2003/011962

4/4

FIG 8

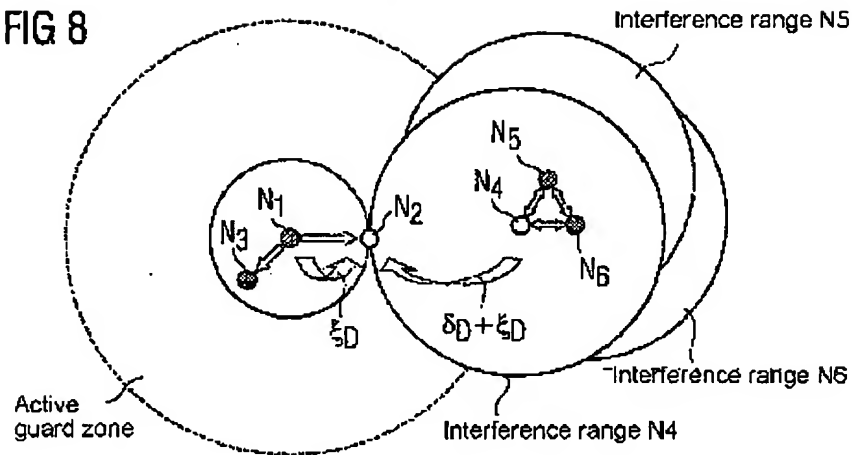


FIG 9

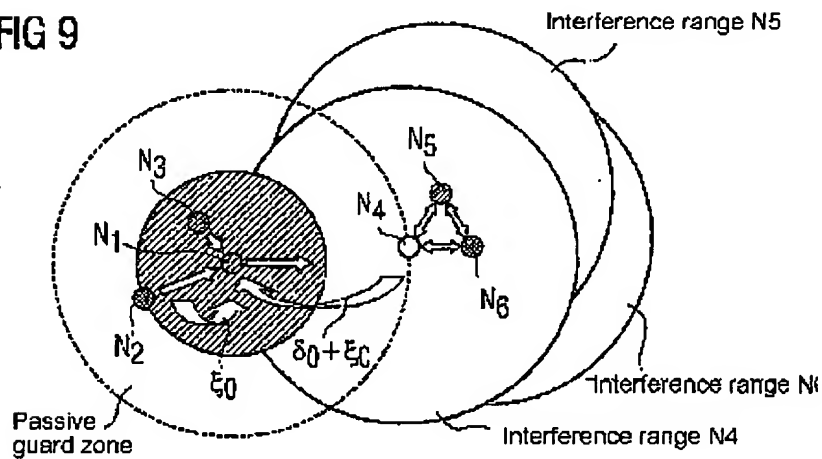
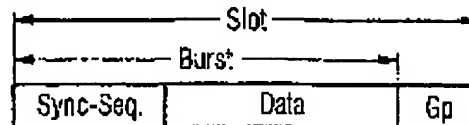


FIG 10



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